M57962L

HYBRID IC FOR DRIVING IGBT MODULES

DESCRIPTION

M57962L is a hybrid integrated circuit designed for driving n-channel IGBT modules in any gate-amplifier application. This device operates as an isolation amplifier for these modules and provides the required electrical isolation between the input and output with an opto-coupler.

Recommended modules:

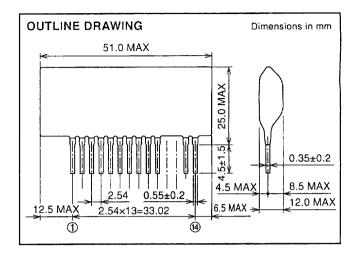
VCES = 600V series upto 400A class VCES = 1200V series upto 400A class

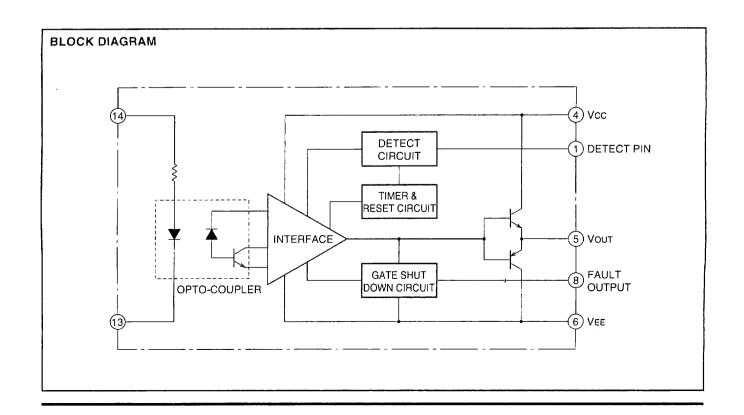
FEATURES

- Electrical isolation between input and output with optocoupler (Viso = 2500Vrms for 1 minute)
- Two supply driver topology
- Built-in short circuit protection circuit (With a pin for fault out)
- TTL compatible input interface

APPLICATION

To drive IGBT modules for inverter or AC servo systems application.





MAXIMUM RATINGS (Ta = 25°C, unless otherwise noted)

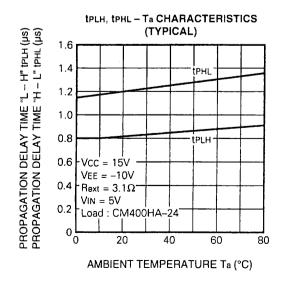
Symbol	Parameter	Conditions	Ratings	Unit
Vcc		DC	18	V
VEE	Supply voltage		. –15	V
Vı	Input voltage	Applied between : 10-13	−1 ~ +7	٧
Vo	Output voltage	Output voltage "H"	Vcc	V
IOHP	Contract and a second	Pulse width 2μs, f = 20kHz	-5	Α
IOLP	Output current		5	Α
Viso	Isolation voltage	Sinewave voltage 60Hz, 1min.	2500	Vrms
Тс	Case temperature		85	°C
Topr	Operating temperature		-20 ~ +60	°C
Tstg	Storage temperature		* -25 ~ +100	°C
Юн	Output current	DC	0.5	Α
IFO	Fault output current	Applied ® pin	20	mA
VR1	Input voltage	Applied ① pin	50	V

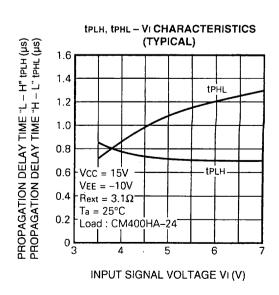
^{*} Differs from H/C condition

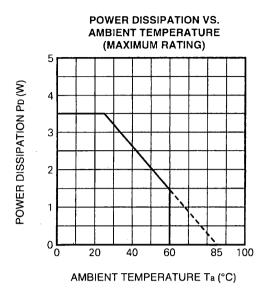
ELECTRICAL CHARACTERISTICS (Ta = 25°C, Vcc = 15V, VEE = -10V, unless otherwise noted)

Symbol	Parameter	Test conditions	Limits			1 (:
			Min	Тур	Max	Unit
Vcc	Supply voltage	Recommended range	14	15	_	V
VEE			-7	_	-10	V
Vin	Pull-up voltage on input side	Recommended range	4.75	5	5.25	V
Iн	"H" input current	$VIN = 5V$, $R = 185\Omega$		16	L	mA
Voн	"H" output voltage		13	14	_	V
VOL	"L" output voltage		-8	-9	_	V
tPLH	"L - H" propagation delay time	Vi = 0 → 4V, Tj = 85°C	_	1	1.5	μs
tr	"L - H" rise time	Vi = 0 → 4V, Tj = 85°C	_	0.6	1	μs
tPHL	"H - L" propagation delay time	Vi = 5 → 0V, Tj = 85°C	_	1	1.5	μs
ti	"H – L" fall time	Vi = 5 → 0V, Tj = 85°C	_	0.4	1	μs
TRESET	Reset time of protection	Between start and cancel (under input sign "L")	1		2	ms
lFO	Fault output current	$R = 4.7k\Omega$		5		mA
Vsc	SC detect voltage		15	_	_	٧

PERFORMANCE CURVES

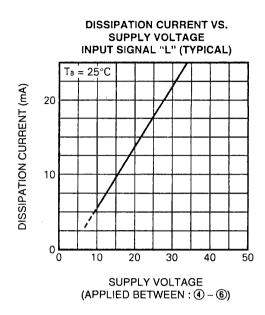




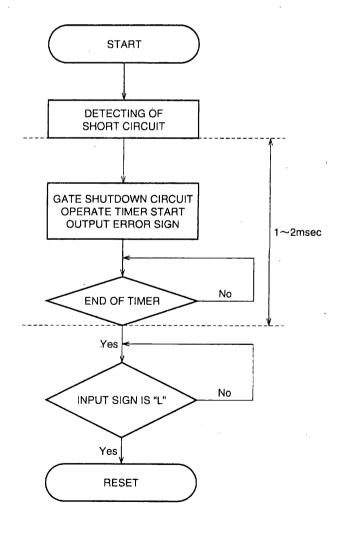


OPERATION OF PROTECTION CIRCUIT

- (1) In case the gate voltage is "H" and the collector voltage is high, this hybrid IC will recognize the circuit as short circuit and immediately reduce the gate voltage. Besides, put out an error sign ("L") which inform that protection circuit is operating at the same time from ® pin.
- (2) The protection circuit reset and resorted to ordinary condition if input sign is "L" when the premised 1~2msec passed. ("L" period needs upto 5μs)

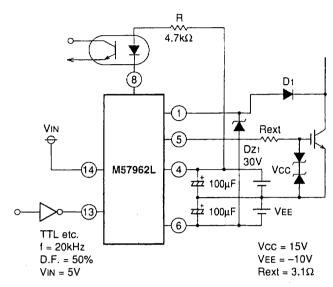


OPERATION FLOW ON DETECTING SHORT CIRCUIT

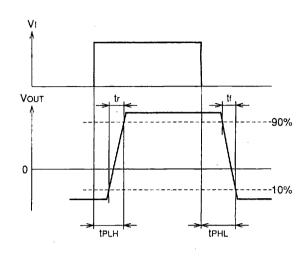


 * "L" output voltage with protection circuit operating is about VEE+2V.

TEST CIRCUIT AND APPLICATION CIRCUIT EXAMPLES



D1 : Fast recovery diode (trr ≤ 0.2μs)



PRECAUTION

- (1) Because the ② ③ ⑦ ⑨ ⑩ pin are the pins connected between surface and back, the usage with electrical connections from the outside are not permitted.
- (2) If reverse recovery time of D1 is long, ① pin is applied high voltage. In that case, counterplan for protection which insert a zener diode between ① and ⑥ pin are necessary like above diagram.
- (3) The hybrid IC will output fault sign ("L") until supply voltage become stable whether supply voltage is on or off.