TOSHIBA GTR Module Silicon N Channel IGBT

MG500Q1US1

High Power Switching Applications Motor Control Applications

• High input impedance

 $\begin{array}{c} \bullet \quad \mbox{High speed} \quad \vdots \ t_f = 0.5 \mu s \ (Max) \\ \\ t_{rr} = 0.5 \mu s \ (Max) \end{array}$

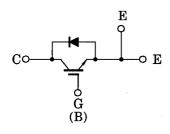
• Low saturation voltage

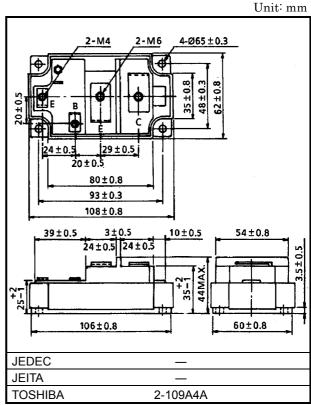
 $V_{CE (sat)} = 4.0 V (Max)$

• Enhancement-mode

• The electrodes are isolated from case

Equivalent Circuit





Weight: 465g

Maximum Ratings (Ta = 25°C)

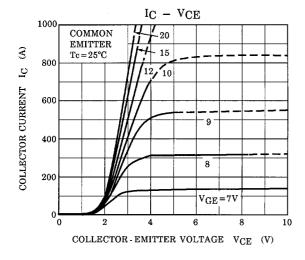
Characteristic		Symbol	Rating	Unit	
Collector-emitter voltage		V _{CES}	1200	V	
Gate-emitter voltage		V _{GES}	±20	V	
Collector current	DC	IC	500	А	
	1ms	I _{CP}	1000		
Forward current	DC	l _F	500	Α	
	1ms	I _{FM}	1000		
Collector power dissipation (Tc = 25°C)		PC	2400	W	
Junction temperature		Tj	150	°C	
Storage temperature range		T _{stg}	-40 ~ 125	°C	
Isolation voltage		V _{Isol}	2500 (AC, 1 min)	V	
Screw torque (Terminal : M4 / M6 / mounting)		_	2/3/3	N·m	

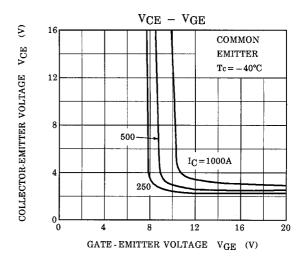
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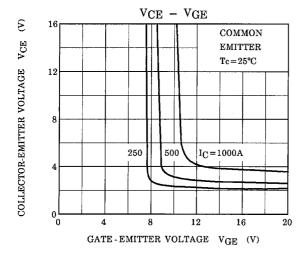
Electrical Characteristics (Ta = 25°C)

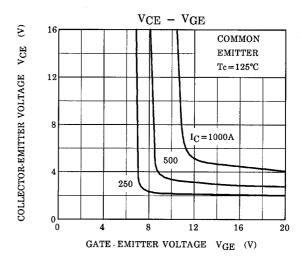
Characteristic Symbol		Test Condition	Min	Тур.	Max	Unit	
Gate leakage current		I _{GES}	V _{GE} = ±20V, V _{CE} = 0	_	_	±500	nA
Collector cut-off c	llector cut-off current I_{CES} $V_{CE} = 1200 V_{GE} = 0$		_	_	4.0	mA	
Gate-emitter cut-off voltage V ₀		V _{GE} (OFF)	I _C = 500mA, V _{CE} = 5V	3.0	_	6.0	V
Collector-emitter	saturation voltage	uration voltage $V_{CE (sat)}$ $I_{C} = 500A, V_{GE} = 15V$		_	3.0	4.0	V
Input capacitance		C _{ies}	V _{CE} = 10V, V _{GE} = 0, f = 1MHz	_	80000	_	pF
Switching time	Rise time	t _r	15V 2.4Ω 3 3 3 3 3 3 3 3 3 3 3 3 3 3 3 3 3 3 3	_	0.3	0.6	- µs
	Turn-on time	t _{on}		_	0.4	0.8	
	Fall time	t _f		_	0.2	0.5	
	Turn-off time	t _{off}		_	0.8	1.5	
Forward voltage		V _F	I _F = 500 A, V _{GE} = 0	_	_	3.0	V
Reverse recovery time		t _{rr}	$I_F = 500 \text{ A}, \ V_{GE} = -10 \text{ V},$ di / dt = 300 A / μ s	_	0.25	0.5	μs
Thermal resistance		R _{th (j-c)}	Transistor	_	_	0.042	°C/W
			Diode	_	_	0.20	

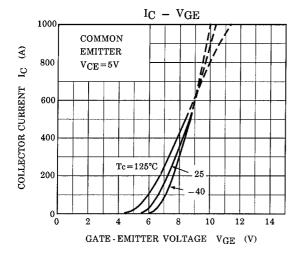
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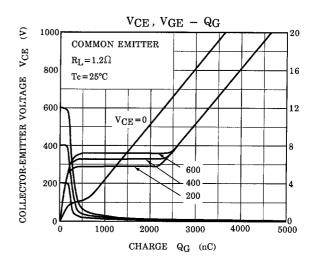


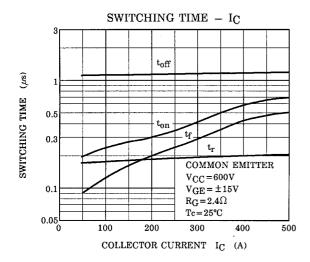


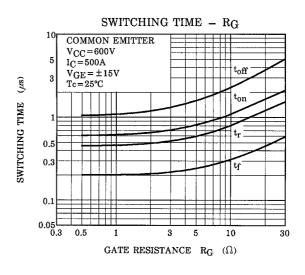


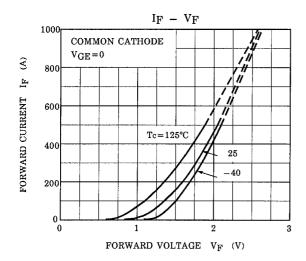


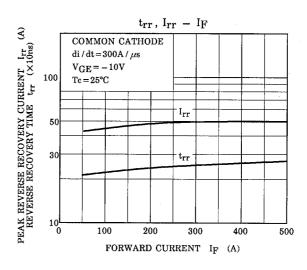


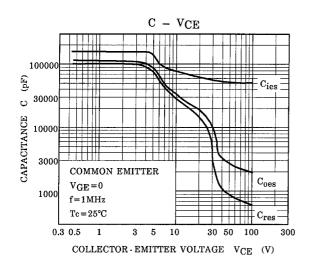


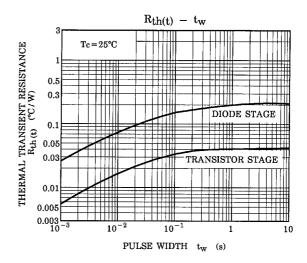




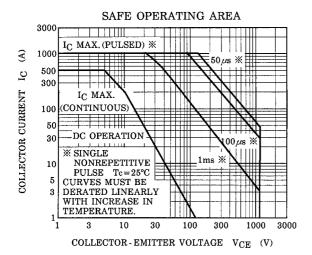


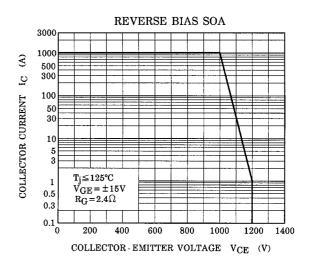






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